


<div style="text-align: center;">  <p>INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)</p> </div>	ATTY DOCKET NO. ITL 0970US (P16133)	SERIAL NO. 10/625,442
	APPLICANT(S): SHIVNANDAN D. KAUSHIK; PING SAGER	
	FILING DATE: July 23, 2003	GROUP ART UNIT: Unknown

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A.					
	B.					
	C.					
	D.					
	E.					
	F.					
	G.					
	H.					
	I.					

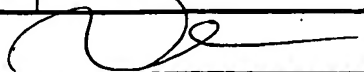
FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
					YES	NO
J.						
K.						
L.						
M.						
N.						
O.						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

A/E	P.	Marr, Deborah T., et al. "Hyper-Threading Technology Architecture and Microarchitecture". Intel Technology Journal, vol. 6, no. 1, February 2002, pp. 4-15.
A/E	Q.	"Mobile Intel® Pentium® III Processors - Intel SpeedStep® Technology" Intel Corporation. http://www.intel.com/support/processors/mobile/pentiumiii/ss.htm . March 24, 2003.
A/E	R.	"Windows Platform Design Notes-Designing Hardware for the Microsoft® Windows® Family of Operating Systems-Windows Native Processor Performance Control". Microsoft® Corporation. November 12, 2003.
	S.	
	T.	
	U.	
	V.	

EXAMINER



DATE CONSIDERED

9/28/05

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.